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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

FEB 2 4 2005

pplicant(s): ISHIO

Serial No.: 10/729,958

Filed: December 9, 2003

Title: METHOD OF PRODUCING

SEMICONDUCTOR DEVICE

Allowed: December 03, 2004

Confirm No.: 2737

Atty. Dkt.: 11-110-DIV

Group Art Unit: 2814

Examiner: RAO, Shrinivas H

Commissioner for Patents Alexandria, VA 22314 Mail Stop Issue Fee February 24, 2005

ISSUE FEE TRANSMITTAL LETTER

Sir:

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Enclosed are one original and one copy of an executed Issue Fee Transmittal Form PTOL-85B for the above allowed patent application, as well as a check in the amount of \$1703 for payment of the requisite issue fee, publication fee and fee for one printed copy of patent.

Authorization is hereby given to charge any fee deficiencies or credit any overpayment to Deposit Account 50-1147.

Respectfully submitted,

David G. Posz Reg. No. 37,701

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